

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (Previously Presented) An apparatus comprising:
a drive device;
a communication bus;
a first processor coupled to the communication bus, the first processor to decode a first stream data including video data and audio data routed over the communication bus;
a second processor provided with a second stream data including video data and audio data that is received from the drive device without being routed over the communication bus, the second processor to decode the second stream data to reproduce the second stream data in accordance with an instruction sent from the first processor over the communication bus.
2. (Previously Presented) The apparatus according to claim 1, wherein the second processor is a stream processor.
3. (Previously Presented) The apparatus according to claim 1, wherein the first processor is a central processing unit (CPU).
- 4-17. (Cancelled).
- 18 (Previously Presented) The apparatus according to claim 1, wherein the drive device is a hard disk drive.
19. (Previously Presented) The apparatus according to claim 1 further comprising:
a network control unit coupled to the communication bus, the network control unit to transmit the first stream data via the communication bus.

20. (Currently Amended) The apparatus according to claim 19, wherein the network control unit includes an IEEE 1394 processor.

21. (Cancelled).

22. (Previously Presented) The apparatus according to claim 1, wherein the communication bus is a Peripheral Component Internet (PCI) bus.

23. (Previously Presented) The apparatus according to claim 1, further comprising:
a video bus; and
a graphic controller in communication with the first processor and the second processor, the graphic controller to convert the decoded first stream data into display video signals and to transmit the display video signals to the second processor over the video bus.

24. (Previously Presented) The apparatus according to claim 23, wherein the second processor superposes the display video signals transmitted over the video bus on a video image generated from the decoded second stream data in accordance with display information transferred from the first processor to the second processor over the communication bus.

25. (Previously Presented) The apparatus according to claim 24, wherein the display information includes information designating a region in a drawing area and a transparency rate at the display video signals on a screen.

26. (Previously Presented) The apparatus according to claim 1, further comprising:
a television tuner adapted to transmit a third stream data to the second processor for storage into a storage medium associated with the drive device.

27. (Previously Presented) The apparatus according to claim 1, further comprising:
a television tuner; and
a transport stream bus coupled to the television tuner and the second processor, the transport stream bus enables transmission of the third stream data to the second processor without using the communication bus.

28. (Currently Amended) An apparatus comprising:
a communication bus;
a drive device;
a video terminal;
a first processor coupled to the communication bus, the first processor to decode a first stream data including video data and audio data sent over the communication bus; and
a second processor coupled to the drive device, the video terminal and the first processor, the second processor being provided with a second stream data including video data and audio data that is sent from the drive device without use of the communication bus, the second processor to (i) decode the second stream data for reproducing the second stream data in accordance with an instruction sent from the first processor via the communication bus and (ii) display video signals, that are based on the decoded first stream data and transmitted by the first processor over a video bus separate from the communication bus, on the video terminal.

29. (Previously Presented) The apparatus according to claim 28, wherein the second processor superposes the display video signals on a video image generated from the decoded second stream data in accordance with display information transferred from the first processor to the second processor through the communication bus.

30. (Previously Presented) The apparatus according to claim 29, wherein the display information includes information designating a region in a drawing area and a transparency rate at the display video signals on a screen.

31. (New) The apparatus according to claim 1, wherein the first stream data is received from a first source and the second stream of data is received from a second source different than the first source.

32. (New) The apparatus according to claim 31, wherein the first stream data is received via a connector being different than the second source being a drive device.

33. (New) The apparatus according to claim 28, wherein the first stream data is received from a source different than the drive device.

34. (New) The apparatus according to claim 28, wherein the first stream data is received from the source being one of a network processor and an IEEE 1394 processor.

35. (New) The apparatus according to claim 1, wherein the first stream data is in an encoded format when routed over the communication bus prior to the first processor decoding the first data stream.